

What is claimed is:

1. A display device comprising:

a matrix display panel including a plurality of driving lines, a plurality of scan lines intersecting with said plurality of driving lines, and a plurality of pixel portions which are arranged at the intersecting portions by said plurality of driving lines and said plurality of scan lines and which each include a series circuit of a bistable element and a light emitting element, wherein when a voltage exceeding a turn-on threshold voltage is applied to said series circuit, said bistable element is in an ON state to allow said light emitting element to electrically connect between a driving line and a scan line associated with said series circuit until a voltage lower than a turn-off threshold voltage is applied to said series circuit, and when a voltage lower than the turn-off threshold voltage is applied to said series circuit, said bistable element is in an OFF state to allow said light emitting element to electrically disconnect between said driving line and said scan line associated with said series circuit until a voltage exceeding the turn-on threshold voltage is applied to said series circuit;

a controller which specifies in order one scan line of said plurality of scan lines in accordance with scan timing of an input image signal, and specifies a driving line corresponding to at least one pixel portion to be driven to emit light on said one scan line in accordance with the input

image signal; and

a driver which applies, every time said one scan line is specified, a first predetermined voltage which is lower than the turn-off threshold voltage, between said one scan line and the specified driving line, and thereafter applies a second predetermined voltage which is higher than the turn-on threshold voltage, between said one scan line and the specified driving line.

2. The display device according to Claim 1, wherein every time said one scan line is specified, said driver applies a third predetermined voltage which is lower than the turn-off threshold voltage, between said one scan line and the remaining driving lines other than the specified driving line, and thereafter applies a fourth predetermined voltage which is lower than the turn-on threshold voltage, between said one scan line and the remaining driving lines.

3. The display device according to Claim 2, wherein said driver includes:

a scan line circuit which applies, every time said one scan line is specified, a reset voltage to said one scan line, and immediately thereafter applies a set voltage to said one scan line; and

every time said one scan line is specified, a driving line circuit which applies, during a period of the application of the reset voltage, a fifth predetermined voltage to the specified driving line and a sixth predetermined voltage which is different from said fifth

predetermined voltage to the remaining driving lines, and which applies, during a period of the application of the set voltage, the sixth predetermined voltage to the specified driving line and the fifth predetermined voltage to the remaining driving lines, and

the difference between the fifth predetermined voltage and the reset voltage is equal to the first predetermined voltage, the difference between the sixth predetermined voltage and the set voltage is equal to the second predetermined voltage, the difference between the sixth predetermined voltage and the reset voltage is equal to the third predetermined voltage, and the difference between the fifth predetermined voltage and the set voltage is equal to the fourth predetermined voltage.

4. The display device according to Claim 1, wherein each of said light emitting elements is an organic electroluminescence elements formed integrally with the bistable element.

5. A display device comprising:

a matrix display panel including a plurality of driving lines, a plurality of scan lines intersecting with said plurality of driving lines, and a plurality of pixel portions which are arranged at the intersecting portions by said plurality of driving lines and said plurality of scan lines and which each include a series circuit of a bistable element and a light emitting element, wherein when a voltage exceeding a turn-on threshold voltage is applied to said

series circuit, said bistable element is in an ON state to allow said light emitting element to electrically connect between a driving line and a scan line associated with said series circuit until a voltage lower than a turn-off threshold voltage is applied to said series circuit, and when a voltage lower than the turn-off threshold voltage is applied to said series circuit, said bistable element is in an OFF state to allow said light emitting element to electrically disconnect between said driving line and said scan line associated with said series circuit until a voltage exceeding the turn-on threshold voltage is applied to said series circuit;

a controller which specifies in order one scan line of said plurality of scan lines in accordance with scan timing of an input image signal, and specifies a driving line corresponding to at least one pixel portion to be driven to emit light on said one scan line in accordance with the input image signal; and

a driver which applies, every time said one scan line is specified, a first predetermined voltage which is higher than the turn-on threshold voltage, between said one scan line and the specified driving line, and thereafter applies a second predetermined voltage which is lower than the turn-off threshold voltage, between said one scan line and the specified driving line.

6. The display device according to Claim 5, wherein every time said one scan line is specified, said driver applies a

third predetermined voltage which is higher than the turn-on threshold voltage, between said one scan line and the remaining driving lines other than the specified driving line, and thereafter applies a fourth predetermined voltage which is lower than the turn-off threshold voltage, between said one scan line and the remaining driving lines.

7. The display device according to Claim 6, wherein said driver includes:

a scan line circuit which applies, every time said one scan line is specified, a reset voltage to said one scan line, and immediately thereafter applies a set voltage to said one scan line; and

every time said one scan line is specified, a driving line circuit which applies, during a period of the application of the reset voltage, a fifth predetermined voltage to the specified driving line and a sixth predetermined voltage which is different from said fifth predetermined voltage to the remaining driving lines, and which applies, during a period of the application of the set voltage, the sixth predetermined voltage to the specified driving line and the fifth predetermined voltage to the remaining driving lines, and

the difference between the fifth predetermined voltage and the reset voltage is equal to the first predetermined voltage, the difference between the sixth predetermined voltage and the set voltage is equal to the second predetermined voltage, the difference between the sixth

predetermined voltage and the reset voltage is equal to the third predetermined voltage, and the difference between the fifth predetermined voltage and the set voltage is equal to the fourth predetermined voltage.

8. The display device according to Claim 5, wherein each of said light emitting elements is an organic electroluminescence elements formed integrally with the bistable element.

9. A method of driving a matrix display panel including a plurality of driving lines, a plurality of scan lines intersecting with said plurality of driving lines, and a plurality of pixel portions which are arranged at the intersecting portions by said plurality of driving lines and said plurality of scan lines and which each include a series circuit of a bistable element and a light emitting element, wherein when a voltage exceeding a turn-on threshold voltage is applied to said series circuit, said bistable element is in an ON state to allow said light emitting element to electrically connect between a driving line and a scan line associated with said series circuit until a voltage lower than a turn-off threshold voltage is applied to said series circuit, and when a voltage lower than the turn-off threshold voltage is applied to said series circuit, said bistable element is in an OFF state to allow said light emitting element to electrically disconnect between said driving line and said scan line associated with said series circuit until a voltage exceeding the turn-on threshold voltage is applied

to said series circuit; said method comprising the steps of:

specifying in order one scan line of said plurality of scan lines in accordance with scan timing of an input image signal, and specifying a driving line corresponding to at least one pixel portion to be driven to emit light on said one scan line in accordance with the input image signal; and

applying, every time said one scan line is specified, a first predetermined voltage which is lower than the turn-off threshold voltage, between said one scan line and the specified driving line, and thereafter applying a second predetermined voltage which is higher than the turn-on threshold voltage, between said one scan line and the specified driving line.

10. A method of driving a matrix display panel including a plurality of driving lines, a plurality of scan lines intersecting with said plurality of driving lines, and a plurality of pixel portions which are arranged at the intersecting portions by said plurality of driving lines and said plurality of scan lines and which each include a series circuit of a bistable element and a light emitting element, wherein when a voltage exceeding a turn-on threshold voltage is applied to said series circuit, said bistable element is in an ON state to allow said light emitting element to electrically connect between a driving line and a scan line associated with said series circuit until a voltage lower than a turn-off threshold voltage is applied to said series circuit, and when a voltage lower than the turn-off threshold

voltage is applied to said series circuit, said bistable element is in an OFF state to allow said light emitting element to electrically disconnect between said driving line and said scan line associated with said series circuit until a voltage exceeding the turn-on threshold voltage is applied to said series circuit; said method comprising the steps of:

specifying in order one scan line of said plurality of scan lines in accordance with scan timing of an input image signal, and specifying a driving line corresponding to at least one pixel portion to be driven to emit light on said one scan line in accordance with the input image signal; and

applying, every time said one scan line is specified, a first predetermined voltage which is higher than the turn-on threshold voltage, between said one scan line and the specified driving line, and thereafter applying a second predetermined voltage which is lower than the turn-off threshold voltage, between said one scan line and the specified driving line.